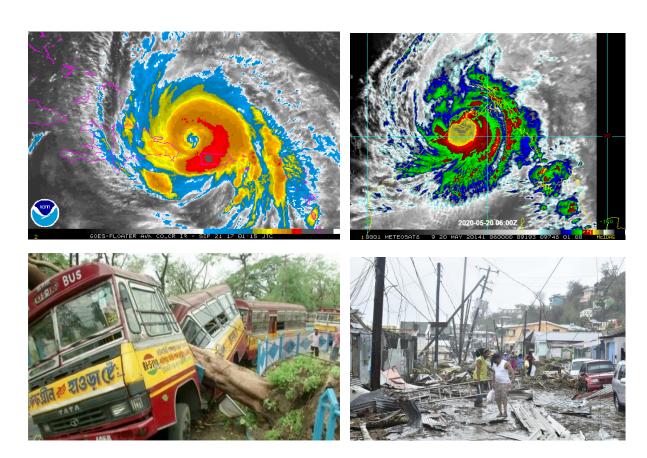
Differential-Power-Processing-Based Microgrid Architectures using Control Co-design



Dr. Arijit Banerjee



Microgrid abstraction from a power processing perspective

dc source



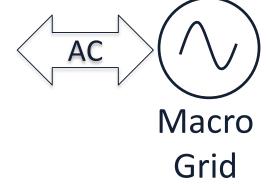
ac source





Storage

Power Corridor (ac/dc or hybrid)





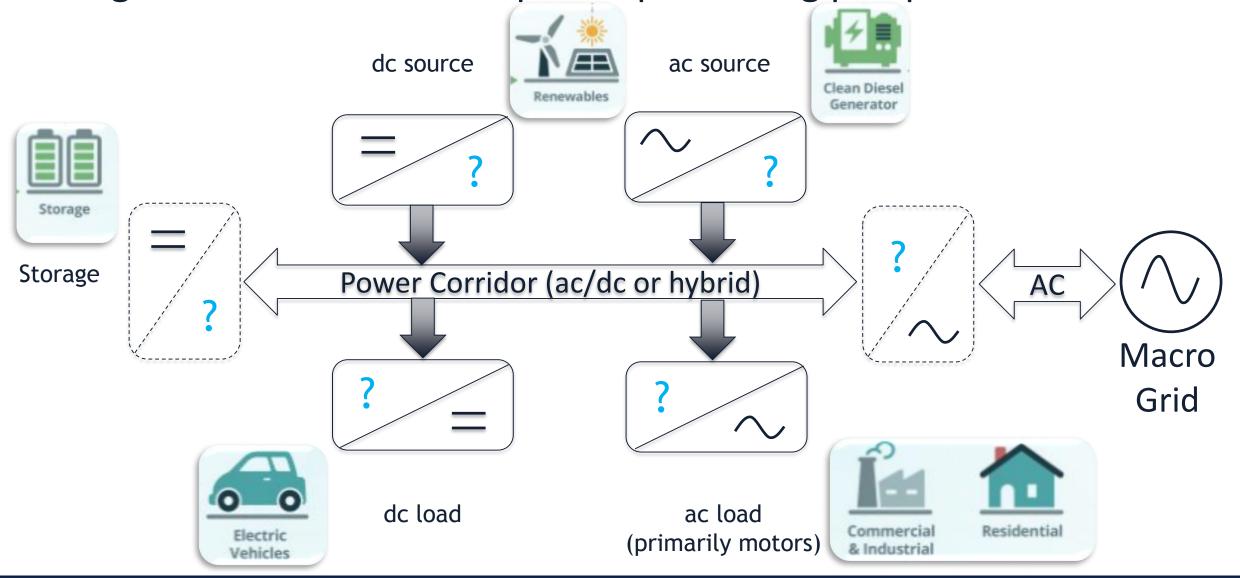
dc load

ac load (primarily motors)



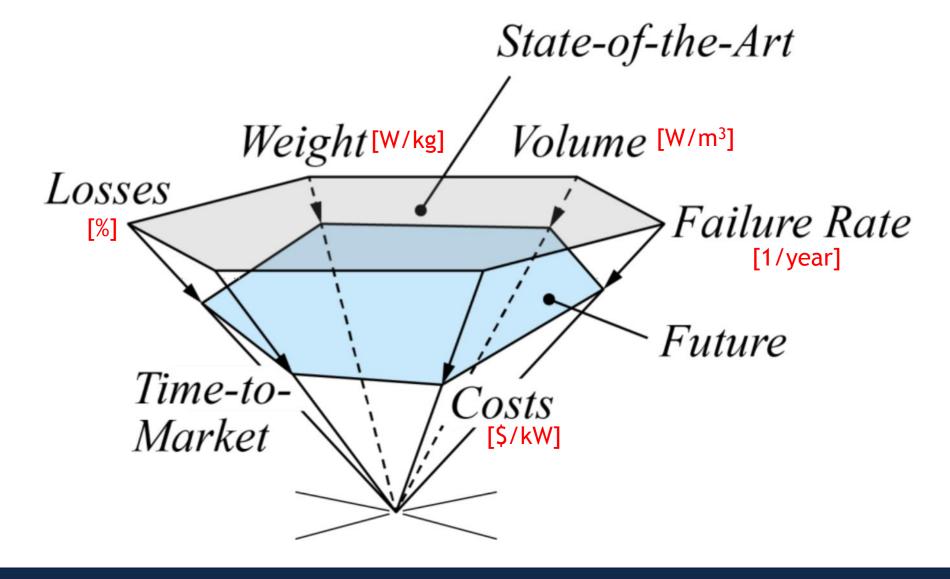


Microgrid abstraction from a power processing perspective



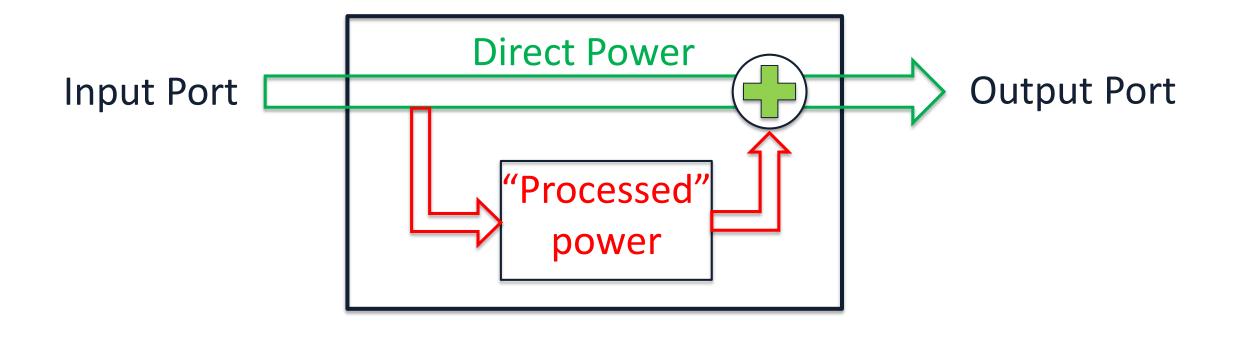


Objective: Improve performance metrics for the power electronics



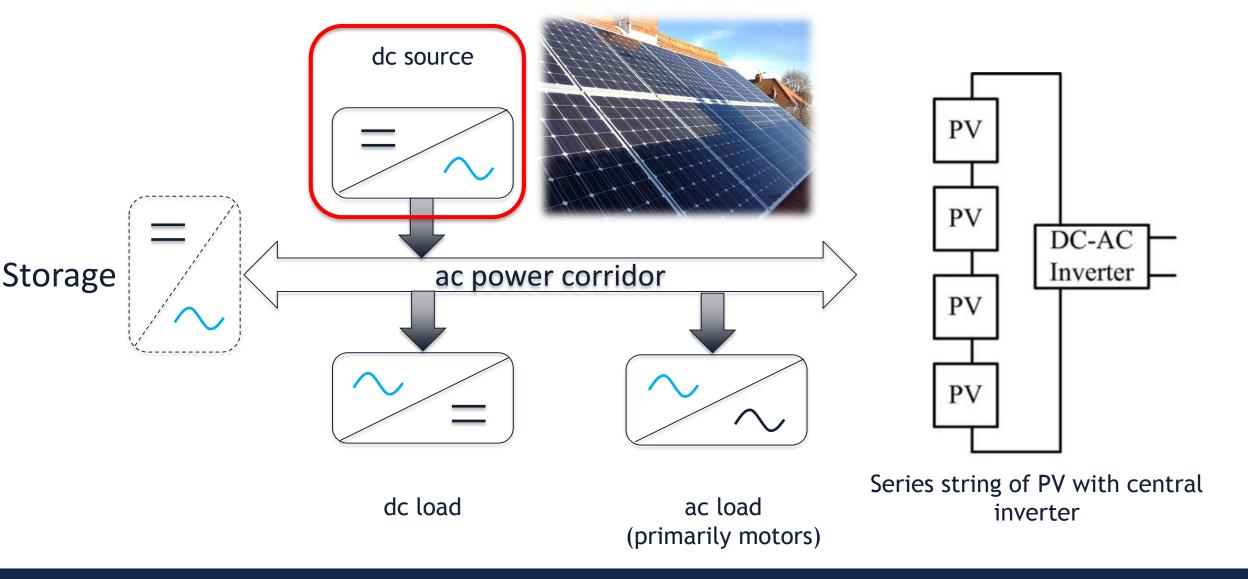


Fundamental limit: Differential power processing



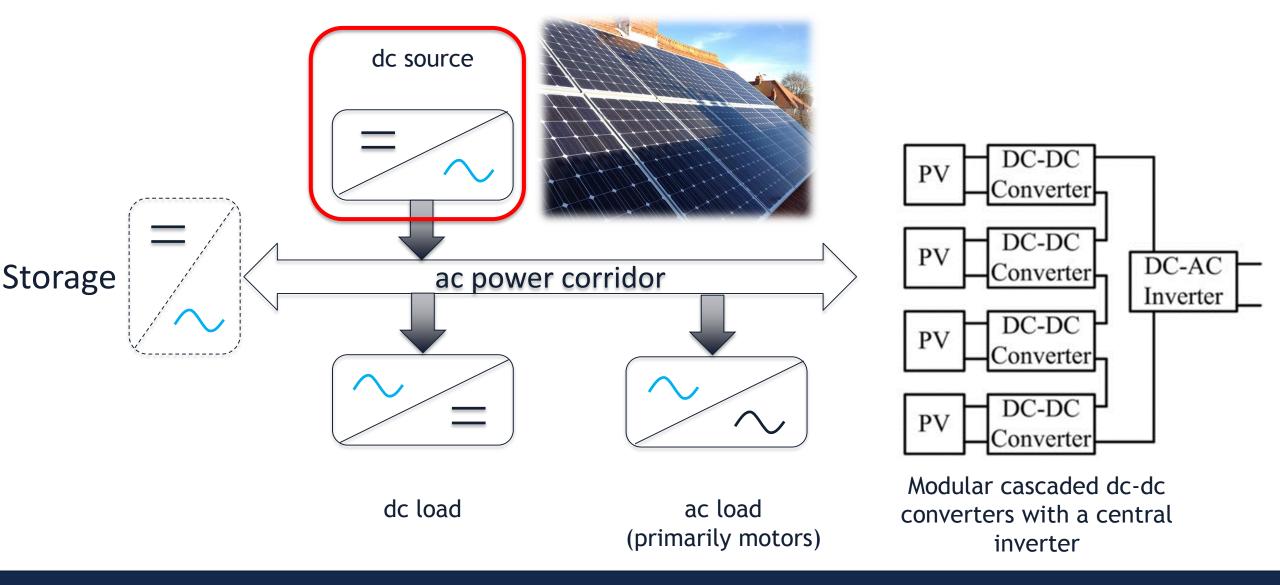


Example 1: Differential power processing for solar + ac power corridor



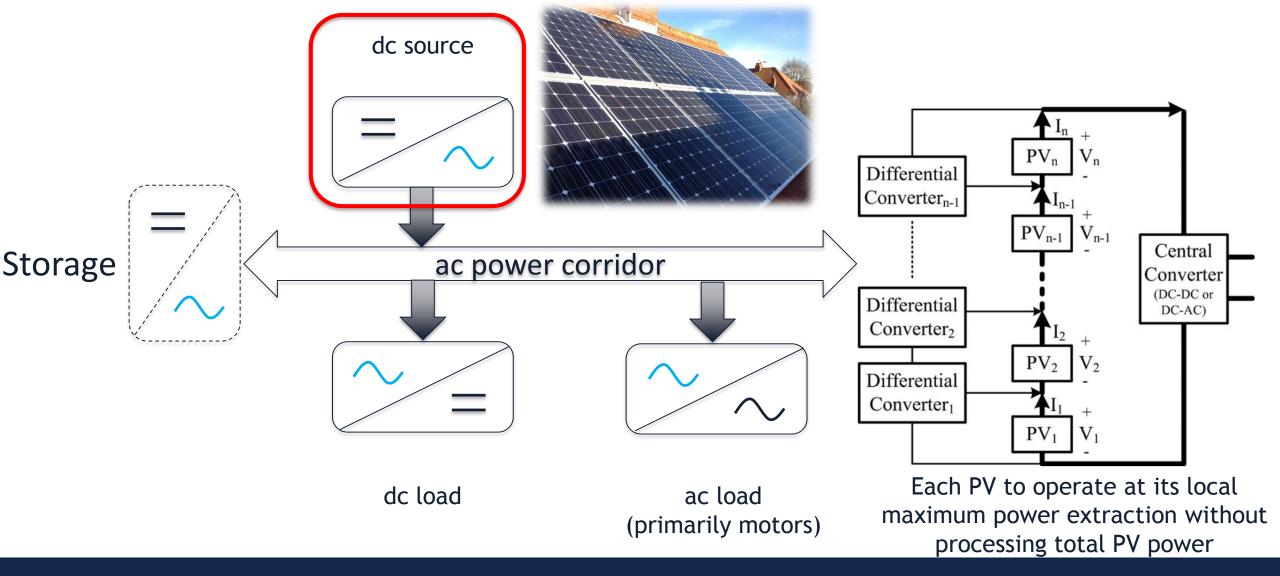


Example 1: Differential power processing for solar + ac power corridor



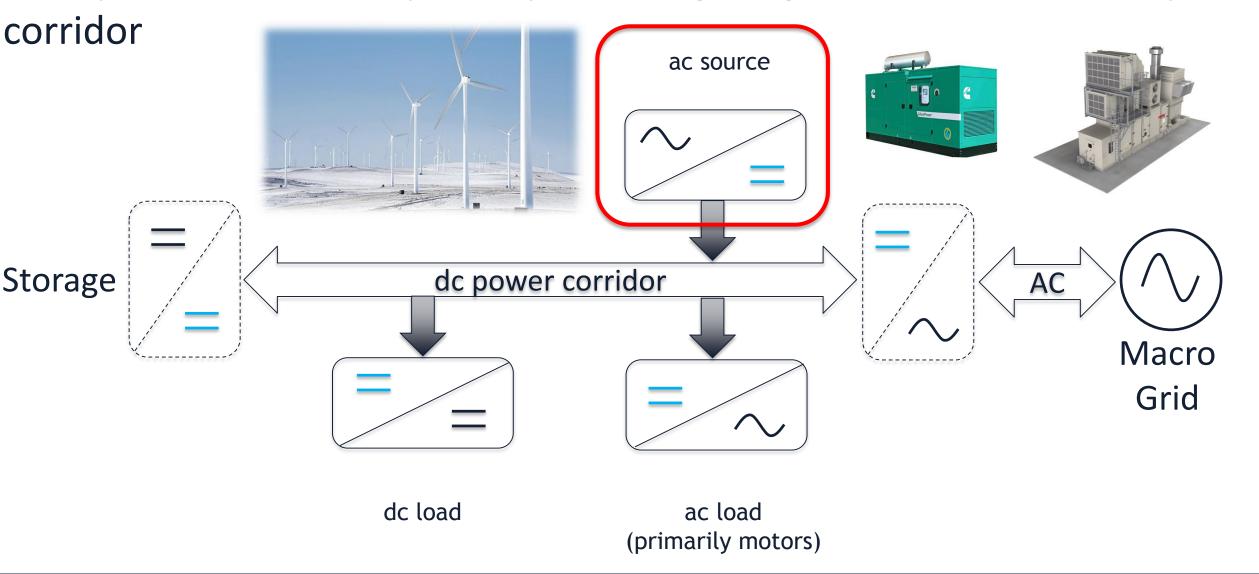


Example 1: Differential power processing for solar + ac power corridor



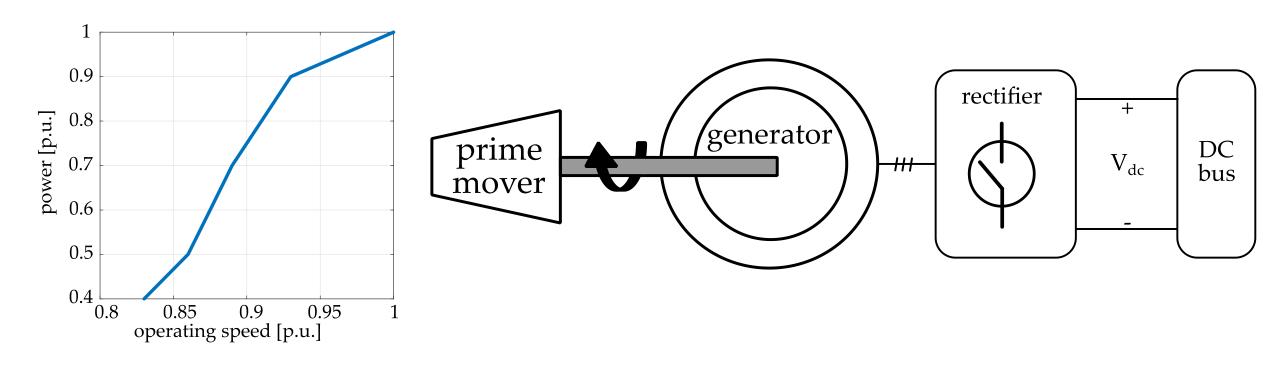


Example 2: Differential power processing for gas/wind turbine + dc power





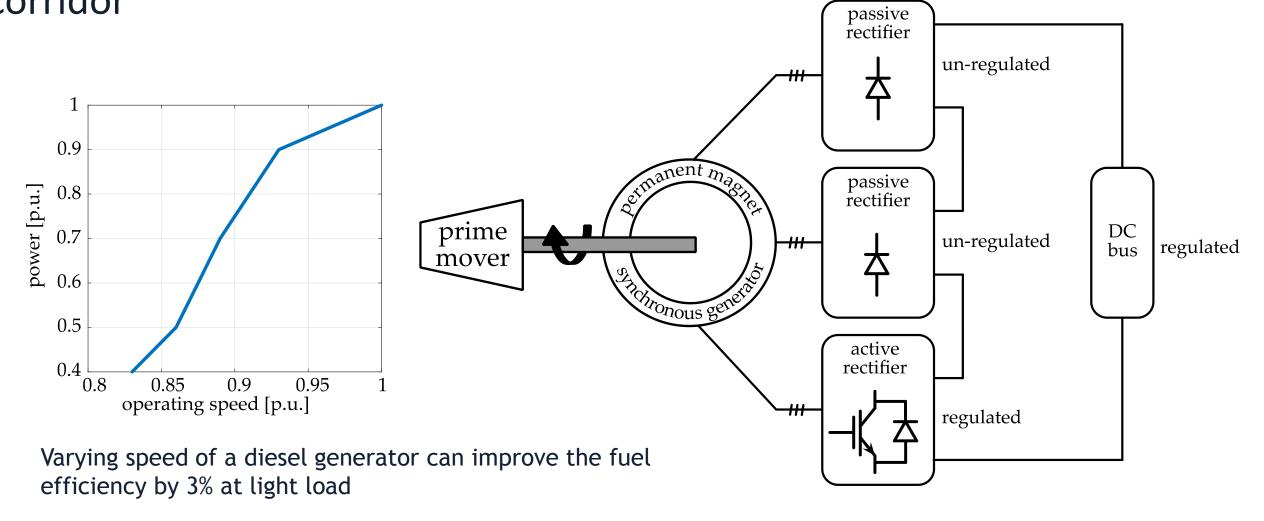
Example 2: Differential power processing for gas/wind turbine + dc power corridor



Varying speed of a diesel generator can improve the fuel efficiency by 3% at light load

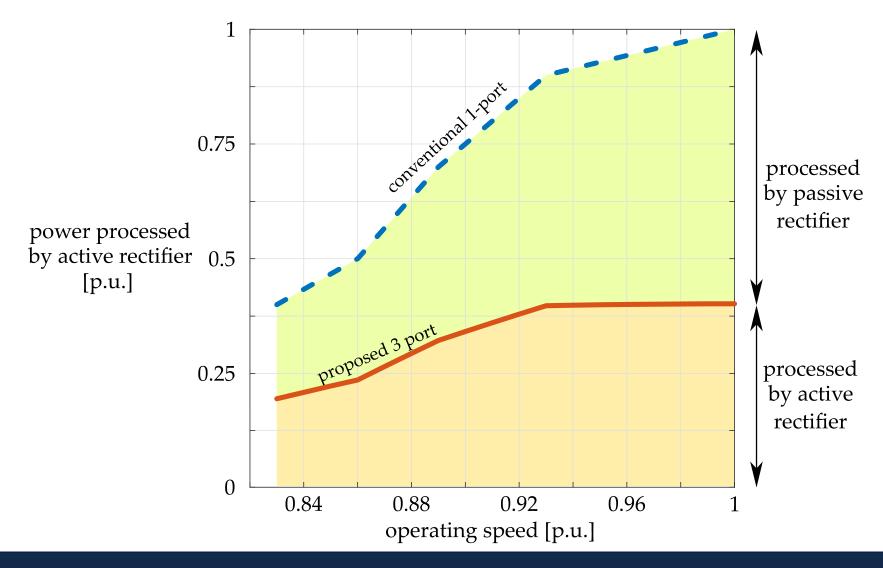


Example 2: Differential power processing for gas/wind turbine + dc power corridor



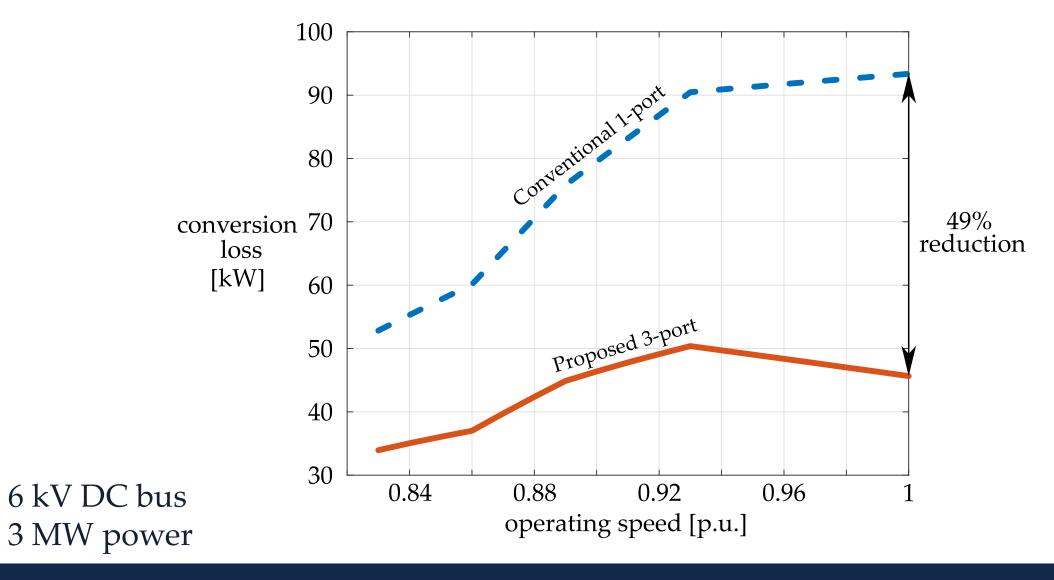


60% of the power processed relatively inexpensively and more reliably





Conversion loss is nearly half at the rated operating condition

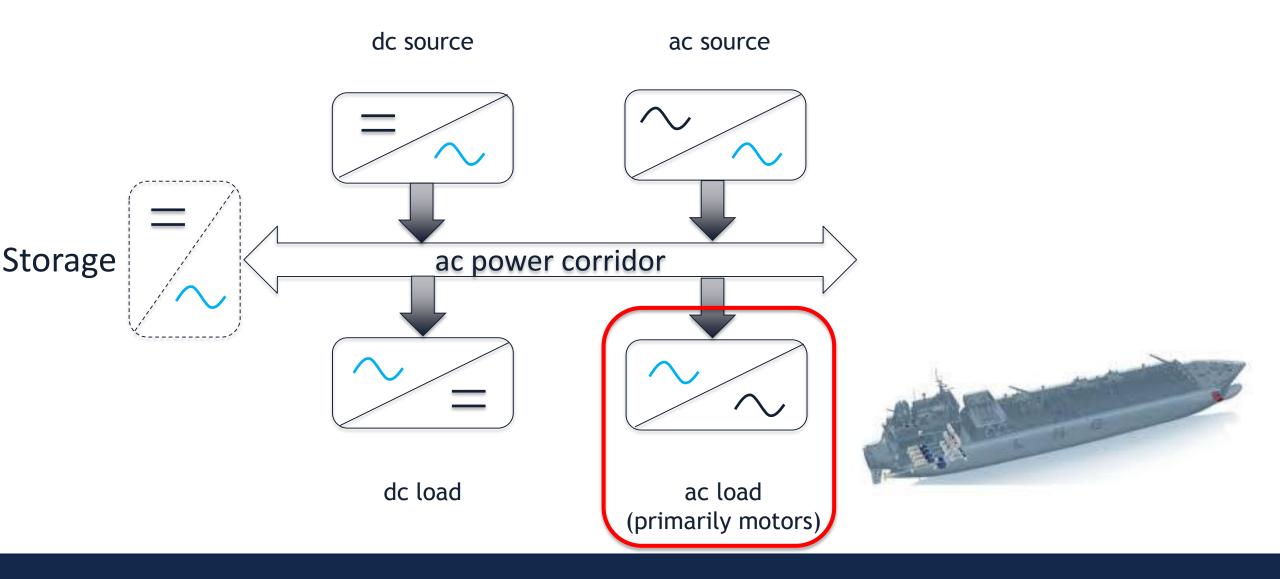




Key enabler: Control Co-design 100 80 Dc-bus power 60 [W] 40 Load 20 /PMSG \mathbf{PM} 150 140 Active-rectifier _ PM speed and position_ Measurement __ power line voltage 130 III three phase [V] -- measurement signal 120 power capability 110 two-level 265 simple reliability control Dc-bus voltage [V] 260 diode bridge 0 2 6 7 Time [s] powér efficiency

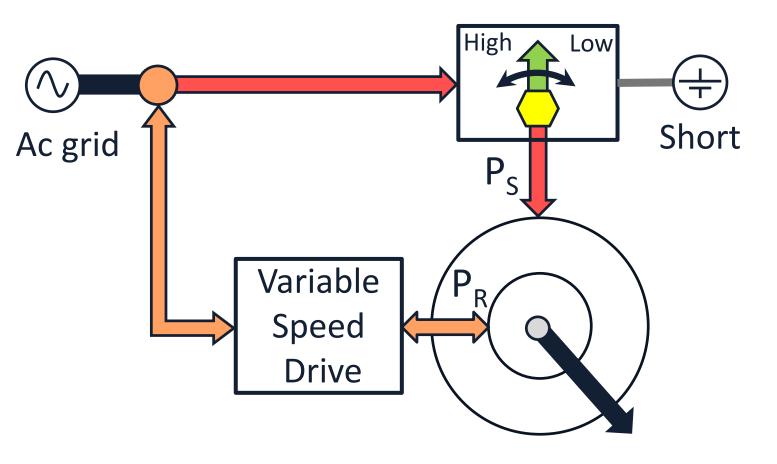


Example 3: ac power corridor + propulsion motor



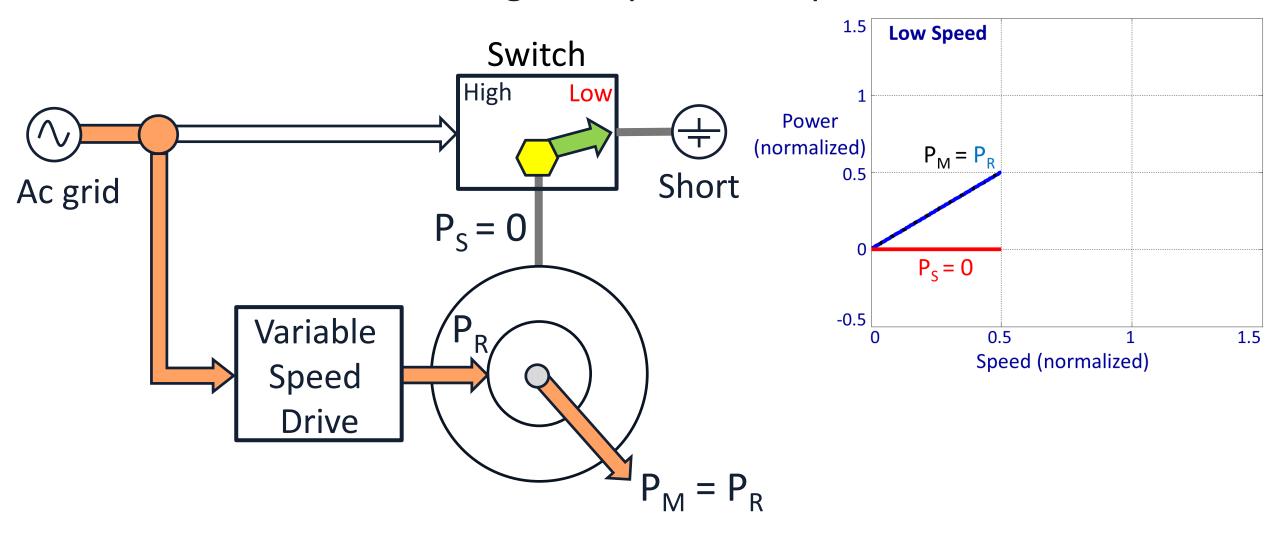


Example 3: ac power corridor + propulsion motor



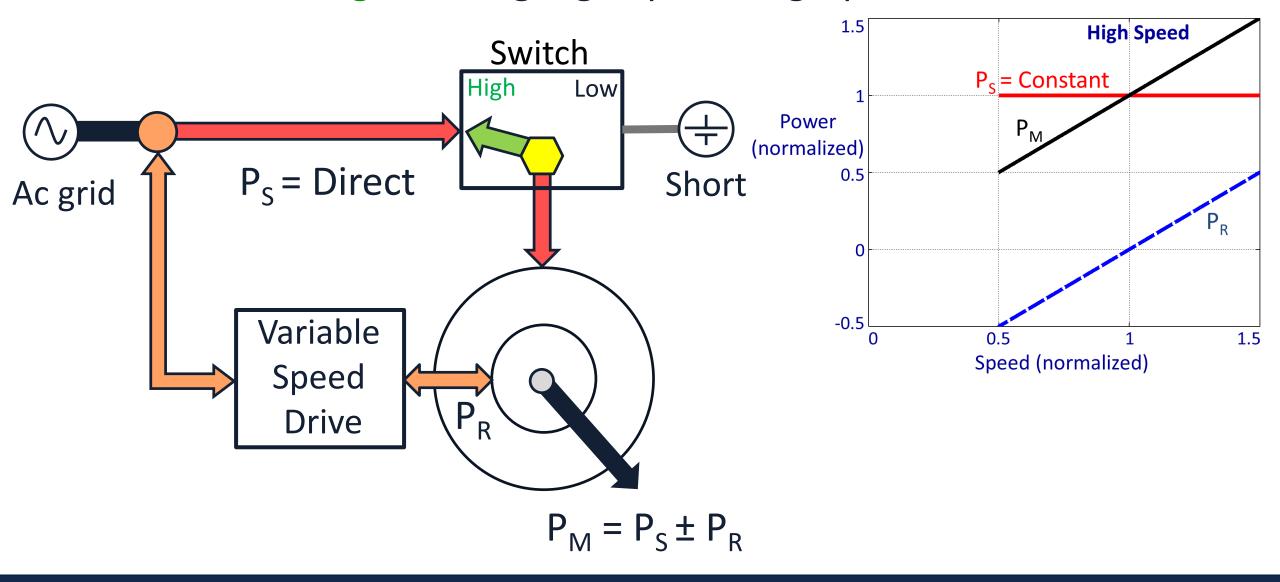


Switch is turned "Low" during low-speed, low-power mode



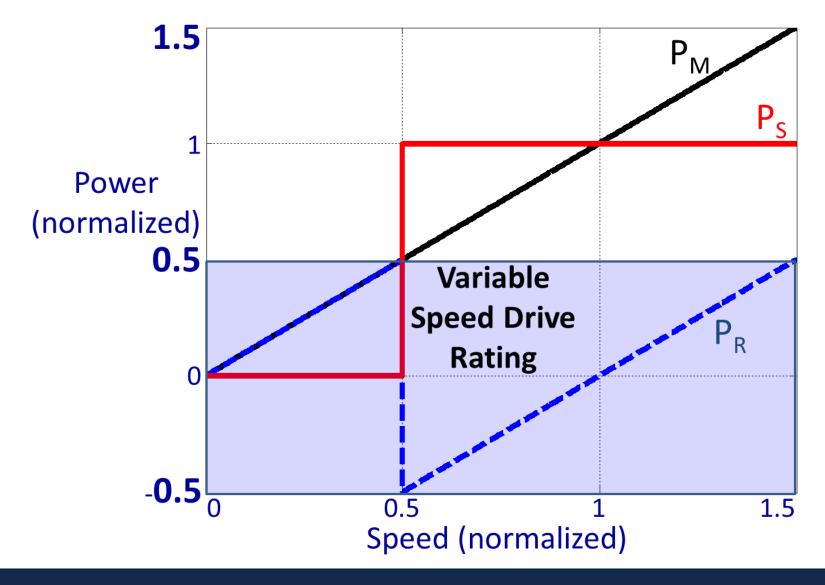


Switch is turned "High" during high-speed, high-power mode





Size of variable speed drive reduces by two-thirds





A. Banerjee, M. S. Tomovich, S. B. Leeb, and J. L. Kirtley, "Control Architecture for a Switched Doubly-Fed Machine Propulsion Drive," in IEEE Transactions on Industry Applications, vol. 51, no. 2, pp. 1538-1550, March-April 2015.

Laboratory-scaled ship power system Low Speed High Speed **Total** Generators (1.4 kW) 1.5 **6** Machines **Power Active** 5 Control platforms (TI, NI, Matlab RTW, PSoC) Power ¹ Stator **3** Data acquisition systems (normalized) Variable 2 Converters + Filters 0.5 Speed Drive Three phase Dc Source CUI VGS-100-24 20 V -0.5 0.53 1.5 Speed (normalized) 600 W Electronic Load Bank Drive End Inverter BK Precision 8512 (Variable voltage (Variable voltage (Controlled current variable frequency) variable frequency) Grid-side Conv. DC Electronic TI C2000 PM Gen. DFM (1.1 kW) Load Bank





Differential/partial processing architecture inherently relies on control co-design

Solutions are case-by-case basis

What kind of power corridor is better?

 How can we have a holistic approach to create microgrid power network architecture?

• How do we take advantage of differential power processing to enable control co-design and achieve control objectives (Ref: Mario's slide)?

